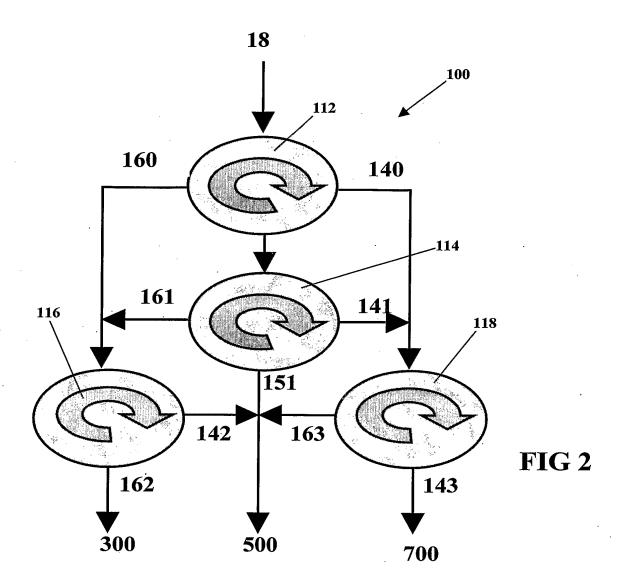


FIG 1







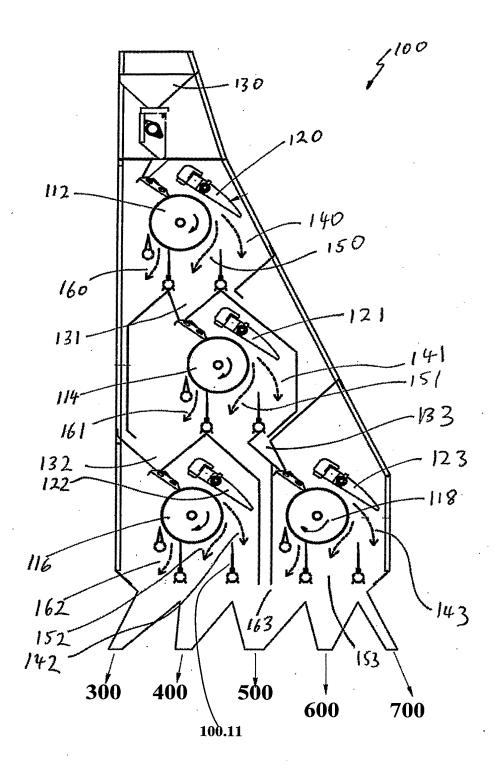
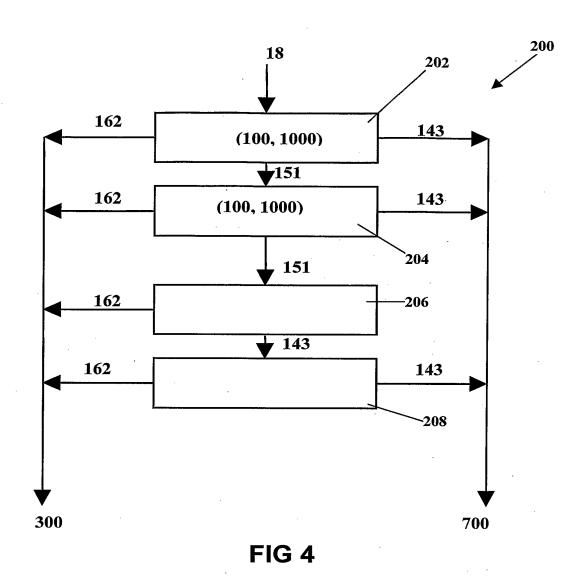
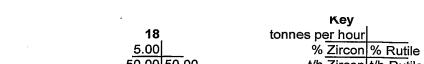
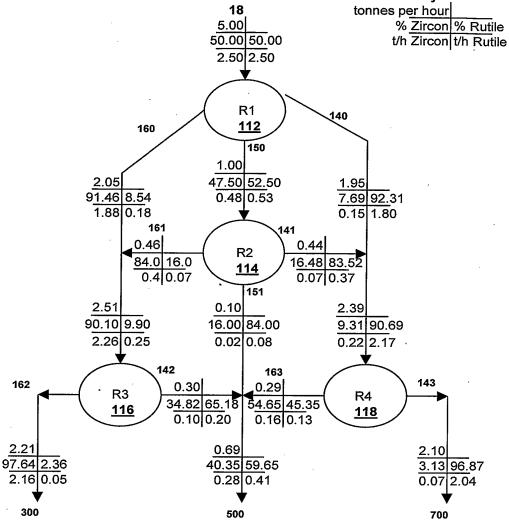


FIG 3

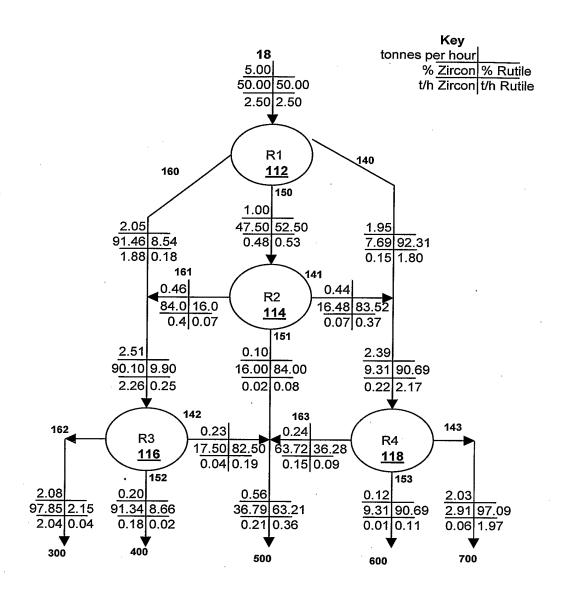






Circuit balance 0.000 0.000 0.000

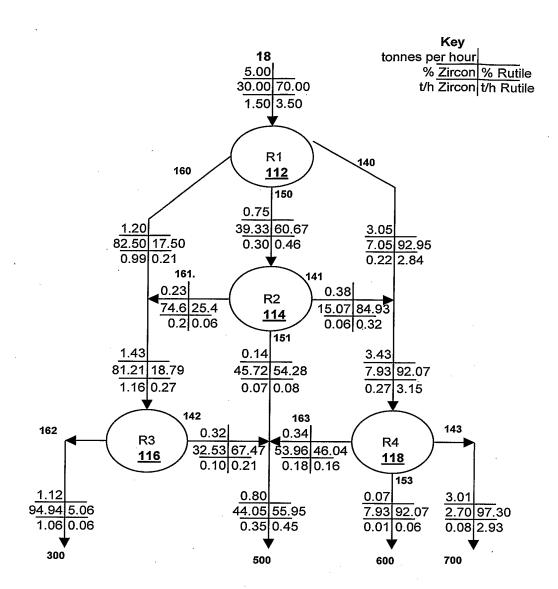
FIG 5



0.000 0.000

FIG 6

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0.000 0.000

**FIG 7** 

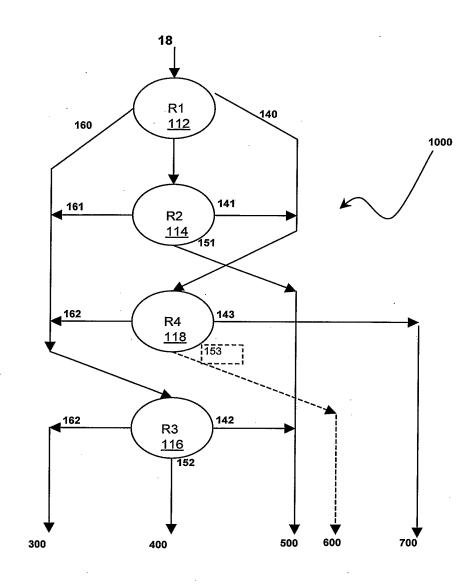


FIG 8

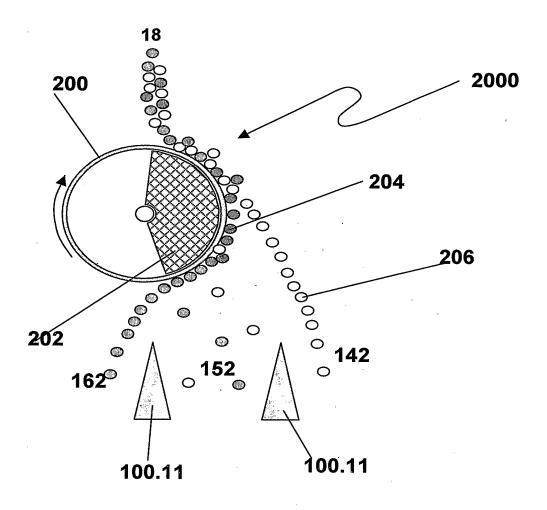
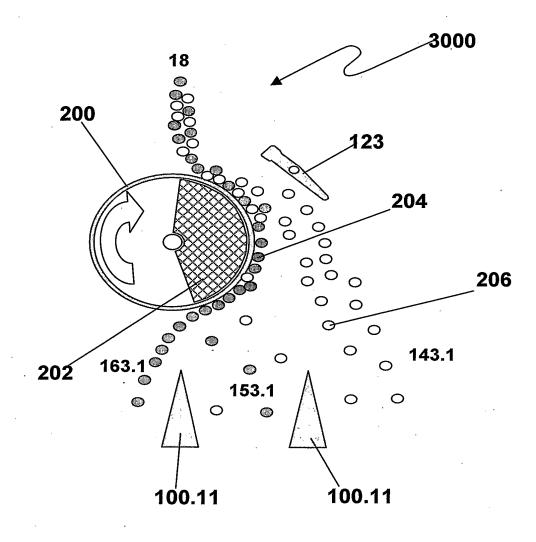
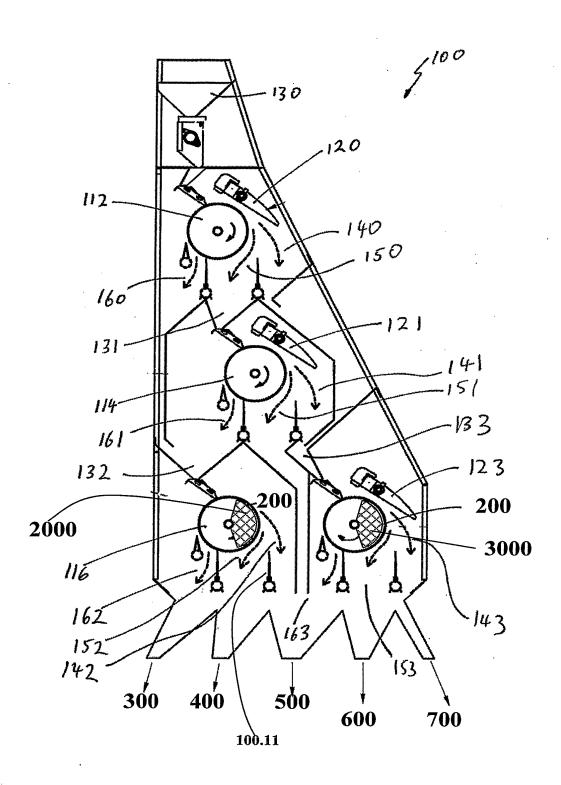


FIG 9



**FIG 10** 



**FIG 11**